



IEEE Chiplet Workshop: Standards, Circuits and Systems Call for Paper and Contribution

Submission Deadline:

Sept. 20th, 2025

[Workshop Introduction](#): The IEEE Chiplet Workshop: Standard, Circuits and Systems (COSIS) is an international event, sponsored by the IEEE Circuit and System Society (CASS) as an ISICAS 2025 satellite event, taking place in Wuhan on October 11th, 2025. This first-time workshop focuses on exploring the higher dimension design challenges and solutions in advanced circuit and system design, emphasizing the latest architecture/circuit/EDA innovations and standards in chiplet technology.

[Author Instructions](#): Authors are required to submit a 1-page poster-like template in PDF format (downloaded from workshop website: <http://2025.ieee-isicas.org/program/cw.html>). All submitted papers must describe original work and will be printed as submitted if accepted. Submissions can include papers already presented at top-tier conferences or published in CASS journals, as well as preprinted versions. However, non-published/pre-printed papers are strongly encouraged to ensure fresh perspectives and innovative contributions. All submissions will be reviewed for quality and relevance to the workshop theme.

[Workshop Publication](#): The workshop will **NOT** require further publication for accepted papers beyond the workshop poster digest. This digest, provided exclusively in hard copy to conference attendees, should not affect any future publication plans, so no copyright transfer is necessary for submitted papers.

[Paper Submission](#): website link: <https://easychair.org/conferences?conf=cosis25>

For further questions, please contact: secretary.chiplet.workshop@gmail.com

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